



## UNITED STATES PATENT AND TRADEMARK OFFICE

COMMISSIONER FOR PATENTS  
UNITED STATES PATENT AND TRADEMARK OFFICE  
WASHINGTON, D.C. 20231  
www.uspto.gov



Bib Data Sheet

<b>SERIAL NUMBER</b> 09/676,197	<b>FILING DATE</b> 09/28/2000 <b>RULE</b> -	<b>CLASS</b> 438	<b>GROUP ART UNIT</b> 2812	<b>ATTORNEY DOCKET NO.</b> 2000.032200	
<b>APPLICANTS</b> William R. Roche, Beaverton, OR ; David Donggang Wu, Austin, TX ; Massud Aminpur, Dresden, GERMANY; Scott D. Luning, Austin, TX ;					
<b>** CONTINUING DATA ***** NONE</b> KBD					
<b>** FOREIGN APPLICATIONS ***** NONE</b> KBD					
<b>IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 11/08/2000</b>					
Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no 35 USC 119 (a-d) conditions <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after met Verified and <i>Thaddeus</i> <b>KBD</b> Acknowledged <i>Examiner's Signature</i> Initials		<b>STATE OR COUNTRY</b> OR	<b>SHEETS DRAWING</b> 3	<b>TOTAL CLAIMS</b> 31	<b>INDEPENDENT CLAIMS</b> 4
<b>ADDRESS</b> J Mike Amerson Williams Morgan & Amerson PC Suite 250 7676 Hillmont Houston, TX 77040					
<b>TITLE</b> Method of forming a gate electrode on a semiconductor device and a device incorporating same					
<b>FILING FEE RECEIVED</b> 966	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time ) <input type="checkbox"/> 1.18 Fees ( Issue ) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit		